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JUN 14 2004****S/N 09/726,629****PATENT****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants:	Ajit V. Sathe et al.	Examiner:	Xuan Marian Thai
Serial No.:	09/726,629	Group Art Unit:	2111
Filed:	November 30, 2000	Docket No.:	884.341US1
Title:	SOLDERLESS ELECTRONICS PACKAGING (As Amended)		
Assignee:	Intel Corporation	Customer No:	21186

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This responds to the Office Action mailed on February 11, 2004. Please amend the above-identified patent application as follows.

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a one-month extension of the period for responding to the Office action, thereby moving the deadline for response from May 11, 2004 to June 11, 2004. Because the U.S. Patent & Trademark Office was closed on June 11, 2004, in honor of President Reagan's funeral, the due date is extended to June 14, 2004.